

Hpe[®]_moduleX1

HMX1-AS2- 60

HMX1-AS2- 90

HMX1-AS2-180

Ready for 

General Description:

The HMX1-AS2 is designed to be used in conjunction with the **Hpe_midi**. A ALTERA Stratix II device is the main part of this module which can be used for the functional verification of high complex digital circuits and for SEmulation. The connection to the main board is done by two 364 pin Elastomer module connectors. The original ALTERA USB Blaster is already on board.



Functional Description

The following features are the most important parts of this FPGA module:

- One 1020 pin ALTERA[®] Stratix II FPGA **EP2S60F1020C3N, EP2S90F1020C3N or EP2S180F1020C3N**
- Two 364 pin Elastomer module connectors (X1, X2) connects the module with the Hpe[®]_midi main board.
- Two 132 pin Elastomer Hpe[®] Child board connectors (X3, X4), one with low voltage differential signals (LVDS) and one for DDR2-SDRAMs or any other Hpe[®]_child board
- Flash for the configuration of the FPGA
- CPLD for clock distribution (clock factory), configuration and fan control (PWM)
- CPLD for FPGA Code Protection (FCP) and ALTERA USB blaster
- Advanced Encryption Standard (AES), programmed on customer request

Block Diagram

